



Attorney's Docket No. 4990 USA/W-C/W-C/JB1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Steven Verhaverbeke et al.

Application No.: 09/891,730

Filed: June 25, 2001

For: CLEANING METHOD AND SOLUTION
FOR CLEANING A WAFER IN A
SINGLE WAFER PROCESS

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

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) Examiner: Umez-Eronini, Lynette T.

) Art Unit: 1765

INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

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has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

X 37 C.F.R. §1.97(b).

_____ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

_____ A statement pursuant to 37 C.F.R. §1.97(e) or

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7/22 2004


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Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

Complete if Known

Application Number	09/891,730
Filing Date	June 25, 2001
First Named Inventor:	Steven Verhaverbeke
Art Unit	1765
Examiner Name	Umez-Eronini, L.

Sheet	1	of 2		Attorney Docket Number BSTZ Docket No.	4990 USA/W-C/W-C/JBI 4887.P447
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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)				
JUL 21 2004 PATENT & TRADEMARK OFFICE U.S. DEPARTMENT OF COMMERCE		US-	3,045,702	07-24-1962	Nakata	
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		US-	6,132,522	10-17-2000	Verhaverbeke et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP 0 245 667 A1	11-19-1987	Edeleanu Gesellschaft mbH		No

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	09/891,730
(use as many sheets as necessary)		Filing Date	June 25, 2001
		First Named Inventor:	Steven Verhaverbeke
		Art Unit	1765
		Examiner Name	Umez-Eronini, L.
Sheet	2	Attorney Docket Number	4990 USA/W-C/W-C/JBI
		bstz Docket No.	4887.P447

O P E R A T I O N S
JUL 27 2004

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		Chapter 3 – Silicon Wafer Cleaning Procedure, http://www-mtl.mit.edu/CAFE/sop_copy/rca.html , 04/24/2002, 4 pages.	
		Patent Abstracts of Japan , Vol. 004, No. 089 (C-016), June 25, 1980 & JP 55 051427 A (Sakaoka Kazuhiko), April 15, 1980, 1 page.	
		Silicon VLSI Technology, Fundamentals, Practice and Modeling, By Plummer, Deal and Griffin, IC Manufacturing-Chapter 4, Semiconductor Manufacturing-Clean Rooms, Wafer Cleaning And Gettering- Chaper 4, ©2000 by Prentice Hall, Upper Saddle River, N.J., 16 pages.	
		Written Opinion for PCT/US/01/41160 mailed April 2, 2004, 7 pages.	
		2 nd Annual International SEMATECH Wafer Cleaning and Surface Preparation Workshop 2000, April 11-12, 2000, Hyatt Hotel, Austin, TX, 24 pages.	

Examiner Signature		Date Considered	
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